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***"X80 4ch Isolated Analog Input IO module"*** FIRMWARE HISTORY
Modules: BMXAMI0410, BMXAMI0410H

| Version # | Date of Publication | Internal reference | Description |
|---|---|---|---|
| SV**1.70** | 08/2021 | PEP0646226R | Fixed channel input value frozen when high EMC disturbance injected in specific condition |

***"X80 4ch Isolated Analog Input IO module"*** FIRMWARE HISTORY
Modules: BMXAMI0410, BMXAMI0410H

| 7/29/2021 | Date of Publication | Internal reference | Description |
|---|---|---|---|
| SV**1.60** | 01/2021 | PEP0590525R | Optimized the 4 status LEDs (RUN, ERR, I/O, DL) blinking fast mechanism.<br>Under a rare condition , when the inconsistence of data in RAM occurs and could not be recovered autonomously, this issue will be indicated by module healthy bit and 4 status LEDs blinking fast. |

# "X80 4ch Isolated Analog Input IO module" FIRMWARE HISTORY

Modules: BMXAMI0410, BMXAMI0410H

| Version # | Date of Publication | Internal reference | Description |
|---|---|---|---|
| **SV1.50** | 04/2020 | PEP0555714R | Optimized acquisition cycle time under quick input signal to be detected.<br>Below is the acquisition cycle time calculation table and a FAQ FA404316 had been published for this explanation: <br><br> *(table below)* |
| | | PEP0473117R | Fixed that deviation exceeds specification in user manual (0.15%FS) when any channel sees fast change input (such as 1Hz sine) signal |

| BMX AMI 0410/0800/0810 | | SV1.0 | SV1.1 | SV1.2 | SV1.3 | SV1.4 (1) | SV1.5 |
|---|---|---|---|---|---|---|---|
| Acquisition cycle time when **below** the slew rate threshold (**T**) (2) | **Normal** (Default) (periodic acquisition for all channels) | 5 ms (AMI04**) / 9 ms (AMI08**) | | | | | |
| | **Fast** (periodic acquisition for the declared channels used) | 1 ms + 1 ms × number of channels used | | | | | |
| Acquisition cycle time when **above** the slew rate threshold | AMI0410 (**T** at normal or fast) | Value drift (3) | T x 2 ms | T x 2 ms | T x 2 ms | NA | 16+(Tx2) ms |
| | AMI0800/0810 (**T** at normal or fast) | Value frozen (4) | T x 2 ms | 8+(Tx2) ms | 8+(Tx2) ms | 16+(Tx2) ms | 16+(Tx2) ms |
| Input signal slew rate threshold (unit: mV/ms or mA/ms) | | $\dfrac{23.84\ mV}{T}$ or $\dfrac{0.09537\ mA}{T}$ | | | | $\dfrac{400\ mV}{T}$ or $\dfrac{1.6\ mA}{T}$ | |

Note:
(1) SV1.4 was only available for BMX AMI 0810RU/HRU
(2) T is the acquisition cycle time when **below** the slew rate threshold, it might be different between Normal and Fast mode. It will be used in the calculation for acquisition time when **above** the slew rate threshold and the calculation of threshold.
(3) For SV1.0 the AMI04** channels contain the value drift (till under range or over range) risk, do NOT use.
(4) For SV1.0 the AMI08** channels contain the value frozen risk, do NOT use.

***"X80 4ch Isolated Analog Input IO module"*** FIRMWARE HISTORY
Modules: BMXAMI0410, BMXAMI0410H

| Version # | Date of Publication | Internal reference | Description |
|---|---|---|---|
| SV**1.30** | 06/2018 | PEP0446999R | Optimized the timing interrupt management coding to eliminate the weakness in the firmware, which could potentially lead to 4 status LEDs (RUN, ERR, I/O, DL) blinking fast. |

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Modules: BMXAMI0410, BMXAMI0410H

| Version # | Date of Publication | Internal reference | Description |
|---|---|---|---|
| SV**1.20** | 05/2014 | PEP0249277R | Fixed that input value deviation and the input channels occur frozen without warning signal from PLC. |

29-Jul-21