Product Advisory; IEC HSBY Logic Mismatch Bit Use Problem

Resolution 1396

What is the Purpose of the Resolution

The purpose of this resolution is to inform Quantum Hot Standby system users of a problem that may occur when working with the logic mismatch bit option in an IEC programmed Hot Standby system

What Customer Activities does this Involve

Maintain
Programming
Upgrade

What Units are Affected

The following units with any “D” suffixed executive software, ie: q58v1xxD.bin (non-A PLC) and q5rv10xD.bin (A version PLC).

140CPU43412 140CPU43412C
140CPU53414 140CPU53414C
140CPU43412A 140CPU43412AC
140CPU53414A 140CPU53414AC

What Documents are Affected

No documents are affected.

Problem Description

Section(s) data will not be transferred from Primary PLC to Standby PLC after executing particular project modifications with the Standby PLC logic mismatch bit set.

The problem occurs when these steps are performed.

1. Set the Primary PLC HSBY Command Register Logic Mismatch bit (13), which allows the Standby PLC to continue running with mismatched logic.
2. Insert/Delete a section or sections between the first and last sections in the Primary PLC project, or change the execution order of the project sections.
3. Download these changes to the Primary PLC

The result of performing these steps is that data in one or more sections will not be transferred from the Primary to the Standby PLC. The section(s) affect will have a red **!** to the left of the section name in the project browser when it is animated.

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IEC HSBY Logic Mismatch Bit Use Problem (cont.)

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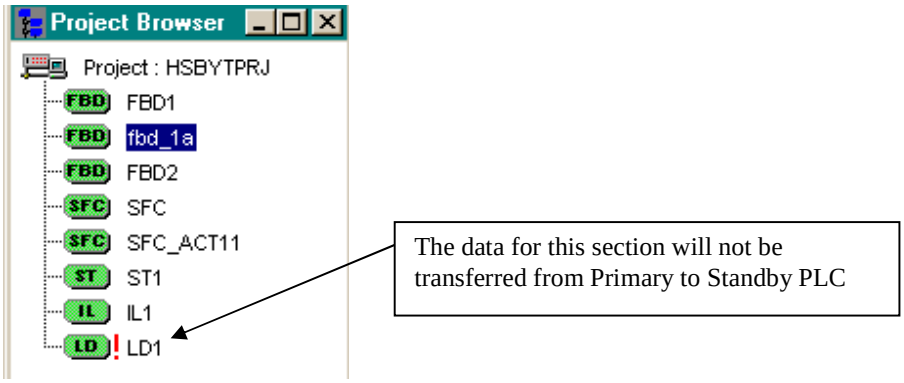

A transfer of the project from Primary to Standby PLC will not correct this condition.

To restore section data transfer the HotStandby System must be taken offline and the following steps performed;

1. Perform a complete download of the project to the Primary PLC.
2. Transfer the project from the Primary to the Standby PLC.

Note: Concept 2.5 offers the feature to selectively remove sections from being transferred to the Standby Controller. This feature may be used to optimize data transfer time. Sections that have been selected to not be transferred will have a black **!** to the left of the section name in the project browser when it is animated.

Section Data Transfer Verification/ Correction

Verification of section data transfer from Primary PLC to Standby PLC can be performed as follows;

1. Connect to the Primary PLC with Concept and your HSBY project open.
2. Open and animate the project browser.
3. The section whose data is not being transferred is marked with a red **!** to the left of the section name.

To restore section data transfer the HotStandby System must be taken offline and the following steps performed;

3. Perform a complete download of the project to the Primary PLC.
4. Transfer the project from the Primary to the Standby PLC.

Note: Concept 2.5 offers the feature to selectively remove sections from being transferred to the Standby Controller. This feature may be used to optimize data transfer time. Sections that have been selected to not be transferred will have a black **!** to the left of the section name in the project browser when it is animated.

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IEC HSBY Logic Mismatch Bit Use Problem (cont.)

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Recommendation

It is recommended that all modifications to a running Hot Standby system that require, or result, in a change of the section execution order be done when system use is not required

Modifications that result in a change of the execution order are

- insertion/deletion of a section between the first and last section of a project
- changing section execution order via the Execution order sub-menu under project menu, or:
- moving sections' location in the project browser.

Resolution

This problem will be corrected in the next release of the Quantum 534/434 & 534/434A PLC executive software. Upon release, the new executive software will be posted on Eclipse, schneiderautomation.com. and Enterprise. Release is currently scheduled for end of 4th quarter 2002.
